PTO/SB/08A (08-03)

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 2

Complete if Known	
Application Number	09/880,734
Filing Date	June 12, 2001
First Named Inventor	Crosland, Andrew
Art Unit	2116
Examiner Name	Tse W. Chen
Attorney Docket Number	15114-053500US

U.S. PATENT DOCUMENTS+

Examiner Initials*	Cite No.[1]	Document Number Number Kind Code[2] (*if known*)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	US-			
	AB	US-			
	AC	US-			
	AD	US-			
	AE	US-			
	AF	US-			

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Country Code[3] Number[4] Kind Code[5] (*if known*)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]
	AG					☐
	AH					☐
	AI					☐
	AJ					☐
	AK					☐
	AL					☐
	AM					☐
	AN					☐

Examiner Signature		Date Considered	

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Sheet 2 of 2

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Application Number	09/880,734
Filing Date	June 12, 2001
First Named Inventor	Crosland, Andrew
Art Unit	2116
Examiner Name	Tse W. Chen
Attorney Docket Number	15114-053500US

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
TWC	AO	DeHON, DPGA-Coupled Microprocessors: Commodity ICs for the Early 21st Century, Artificial Intelligence Laboratory, Massachusetts Institute of Technology, Cambridge, MA, IEEE, February, 1994, pp. 31-33	
TWC	AP	HAUSER, and WAWRZYNEK, University of California, Berkeley, "Garp: A MIPS Processor with a Reconfigurable Coprocessor," IEEE, April 1997, pp. 12-21.	
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TWC	AR	NAGEL, "ACM Computing Surveys: Synergy Between Software and Hardware," Carnegie Mellon University, Department of Electrical Engineering and Computer Science, Pittsburgh, PA, December, 1996, pp. 1-3.	
TWC	AS	RAZDAN, and SMITH, "A High-Performance Microarchitecture with Hardware-Programmable Functional Units," Harvard University, Cambridge, MA, Digital Equipment Corporation, Hudson, MA, November, 1994, pp. 172-180.	
TWC	AT	WITTIG, and CHOW, "OneChip" An FPGA Processor With Reconfigurable Logic," Department of Electrical and Computer Engineering, University of Toronto, Ontario, Canada, IEEE, September, 1996, pp. 126-135.	
TWC	AU	WYNN, "In-Circuit Emulation for ASIC-Based Designs," Xilinx, Inc., VLSI Systems Design, October, 1986, pp. 38-39, and 42-45.	
	AV		
	AW		
	AX		
	AY		

Examiner Signature		Date Considered	9/23/04

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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0

Title of Invention	Embedded processor with watchdog timer for programmable logic

Application Number: 09/880734
Confirmation Number: 4950
First Named Applicant: Andrew Crosland
Attorney Docket Number: 015114-053500US
Art Unit: 2116
Examiner: Tse W Chen
Search string: (5687325 or 6260087).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
TWC	1	5687325	1997-11-11	Web Chang			
TWC	2	6260087	2001-07-10	Web Chang			

Signature

Examiner Name	Date
TSE CHEN	9/23/04

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0

Title of Invention	Embedded processor with watchdog timer for programmable logic

Application Number: 09/880734
Confirmation Number: 4950
First Named Applicant: Andrew Crosland
Attorney Docket Number: 015114-053500US
Art Unit: 2116
Examiner: Tse W. Chan
Search string: (6467009 or RE34444 or 5970254).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
twc	1	6467009	2002-10-15	Winegarden			
twc	2	RE34444	1993-11-16	Kaplinsky			
twc	3	5970254	1999-10-19	Cooke			

Signature

Examiner Name	Date
TSE CHEN	9/23/04

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 2

Complete if Known	
Application Number	09/880,734
Filing Date	June 12, 2001
First Named Inventor	Crosland, Andrew, et. al.
Art Unit	2819
Examiner Name	To Be Assigned
Attorney Docket Number	015114-053500US

U.S. PATENT DOCUMENTS

Examiner	Cite No.[1]	Document Number Number Kind Code[2] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
TWC	0001	US-4,488,259	12-11-1984	Mercy	
TWC	0002	US-4,617,479	10-14-1986	Hartmann et al.	
TWC	0003	US-4,710,927	12-01-1987	Miller	
TWC	0004	US-4,871,930	10-03-1989	Wong et al.	
TWC	0005	US-5,241,224	08-31-1993	Pedersen et al.	
TWC	0006	US-5,258,668	11-02-1993	Cliff et al.	
TWC	0007	US-5,260,610	11-09-1993	Pedersen et al.	
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TWC	0009	US-5,412,260	05-02-1995	Tsui et al.	
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TWC	0012	US-5,790,479	08-04-1998	Conn	
TWC	0013	US-6,097,211	08-01-2000	Couts-Martin et al.	
TWC	0014	US-6,233,205	05-15-2001	Wells et al.	

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Examiner Initials*	Cite No.[1]	Foreign Patent Document Country Code[3] Number[4] Kind Code[5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]
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Examiner Signature		Date Considered	9/23/04

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Sheet 1 of 1

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Application Number	09/880,734
Filing Date	June 12, 2001
First Named Inventor	Crosland, Andrew
Group Art Unit	2819
Examiner Name	Unassigned
Attorney Docket Number	015114-053500US

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TWC	01	"Triscend E5 Configurable System-on-Chip Family," Product Description from Triscend Corporation, January, 2000 (Version 1.00), pp. i-ii and 1-90.	
TWC	02	"Motorola Technical Developments," Magazine of Motorola, Inc., Vol. 39, September 1999, pp. i-vii and 77-80.	
TWC	03	"AT94K Series Field Programmable System Level Integrated Circuit," Advance Information Brochure of Atmel Corporation, December 1999, 6 pages.	
TWC	04	"CS2000 Reconfigurable Communications Processor Family Product Brief," Advance Product Information from ChameleonSystems, Inc., 2000, pages 1-8	
TWC	05	"Wireless Base Station Design Using Reconfigurable Communications Processors," Wireless Base Station White paper from ChameleonSystems, Inc., 2000, pages 1-8.	

Examiner Signature		Date Considered	9/22/04

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TWC	0015	AITKEN, R.C., and AGARWAL, V.K., "A Diagnosis Method Using Pseudo-Random Vectors Without Intermediate Signatures," Proc. of Int. Conf. on Computer-Aided Design (ICCAD), IEEE pp. 574-577 (1989).	
TWC	0016	Altera "APEX 20K Programmable Logic Device Family Data Sheet," May 1999, 7 pages total.	
TWC	0017	Altera "FLEX 10K Embedded Programmable Logic Family Data Sheet," May 1999, 7 pages total.	
TWC	0018	Altera "FLEX 8000 Programmable Logic Device Family Data Sheet," May 1999, 5 pages total.	
TWC	0019	Altera "IEEE 1149.1 (JTAG) Boundary-Scan Testing in Altera Devices," August 1999, Application Note 39, 29 pages total.	
TWC	0020	Altera "MAX 7000 Programmable Logic Device Family Data Sheet," May 1999, 6 pages total.	
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TWC	0023	MC ANNEY, M.G. and SAVIR, J., "There is Information in Faulty Signatures," Proc. of International Test Conf., IEEE, pp. 630-636 (1987).	

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